

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	156772	(line near9 width)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:08
L2	2772	(line near9 width) same overlap\$4 same extend\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:09
L3	2057	((line near9 width) same overlap\$4 same extend\$4) and length	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:09
L4	8	((line near9 width) same overlap\$4 same extend\$4) and length and polygon and terminal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:10
L5	406	((line near9 width) same overlap\$4 same extend\$4) and length and terminal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:10
L6	241	((line near9 width) same overlap\$4 same extend\$4) and length and terminal and center	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:11
L7	7	((line near9 width) same overlap\$4 same extend\$4) and length and terminal and center and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:13
L8	49	((line near9 width) same overlap\$4 same extend\$4) and length and terminal and center and stretch\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 09:11

	Document ID	Title	Current OR
1	US 20040199892 A1	Layout design system, layout design method and layout design program of semiconductor integrated circuit, and method of manufacturing the same	716/12
2	US 20030079194 A1	Method of automatic layout design for LSI, mask set and semiconductor integrated circuit manufactured by automatic layout design method, and recording medium storing automatic layout design program	716/8
3	US 20030005399 A1	Layout design system, layout design method and layout design program of semiconductor integrated circuit, and method of manufacturing the same	716/8
4	US 6813756 B2	Method of automatic layout design for LSI, mask set and semiconductor integrated circuit manufactured by automatic layout design method, and recording medium storing automatic layout design program	716/14
5	US 6763508 B2	Layout design system, layout design method and layout design program of semiconductor integrated circuit, and method of manufacturing the same	716/8
6	US 6546540 B1	Method of automatic layout design for LSI, mask set and semiconductor integrated circuit manufactured by automatic layout design method, and recording medium storing automatic layout design program	716/13
7	US 5905654 A	Method and device for designing layout of milliwave or microwave integrated circuit	716/9